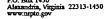


## United States Patent and Trademark Office

UNITED STATES DEPARTMENT OF COMMERCE United States Patent and Trademark Office Address: COMMISSIONER FOR PATENTS P.O. Box 1450 Alsandria, Virginia 22313-1450 www.nspto.gov





## **CONFIRMATION NO. 4914**

SERIAL NUMBEF 10/618,704	FILING DATE 07/15/2003 RULE	CLASS 716	GROUP ART 2825	UNIT	ATTORNEY DOCKET NO. 8009-1014	
APPLICANTS						
Keisuke Kanamaru, Tokyo, JAPAN;						
Ko Yoshikawa, Tokyo, JAPAN;						
** CONTINUING DATA **********************************						
₩ FOREIGN APPLICATIONS ************************************						
IF REQUIRED, FOREIGN FILING LICENSE GRANTED ** 10/14/2003						
Foreign Priority claimed 35 USC 119 (a-d) conditio met Verified and Acknowledged	Allowance Na		SHEETS DRAWING 12	TOTA CLAIM 15		
ADDRESS 000466 YOUNG & THOMPSON 745 SOUTH 23RD STREET 2ND FLOOR ARLINGTON, VA						
TITLE Delay optimization designing system and delay optimization designing method for a logic circuit and control program						
				☐ All Fees		
				1.16 Fees (Filing)		
FILING FEE FE	ES: Authority has been gi to charge/cr	ven in Paper edit DEPOSIT ACCOU	NT time )	1.17 Fees ( Processing Ext. of time )		